



Receipt  
10-10-01  
EHR

PATENT  
Attorney Docket No. 2207/11273

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: FLETCHER et al  
Application No.: 09/893,871  
Filed: June 29, 2001  
For: MULTISTAGE CLOCK DELAY CIRCUIT AND METHOD  
Examiner: Not assigned  
Group Art: 2181

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Commissioner of Patents and Trademarks  
Washington D.C. 20231

SEP 06 2001

Technology Center 2100

REQUEST FOR CORRECTED FILING RECEIPT

Sir:

Applicant for the above-referenced patent application respectfully requests issue of a corrected Filing Receipt to correct a typographical error in the title on the Filing Receipt.

The first word in the title, "MUTLISTAGE", should be "MULTISTAGE". The correct title is "MULTISTAGE CLOCK DELAY CIRCUIT AND METHOD".

Attached is a copy of the Filing Receipt showing the correction in red ink.

These corrections are readily apparent from the application papers.

A corrected filing receipt is respectfully requested.

Respectfully submitted,

KENYON & KENYON

Date: Aug, 27, 2001

Kenneth R. Corsello  
Registration No. 38,115



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
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 WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/893,871	06/29/2001	2182	1076	2207/11273	4	27	6

23838  
 KENYON & KENYON  
 1500 K STREET, N.W., SUITE 700  
 WASHINGTON, DC 20005



## FILING RECEIPT



'OC000000006459155'

Date Mailed: 08/22/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Custom Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Domestic Priority data as claimed by applicant

SEP 06 2001

## Foreign Applications

Technology Center 2100

If Required, Foreign Filing License Granted 08/22/2001

Projected Publication Date: 01/02/2003

Non-Publication Request: No

Early Publication Request: No

Title

Multistage

Multistage clock delay circuit and method

Preliminary Class

713

Data entry by : MUSLIM, ALIA

Team : OIPE

Date: 08/22/2001

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 E/0824/01